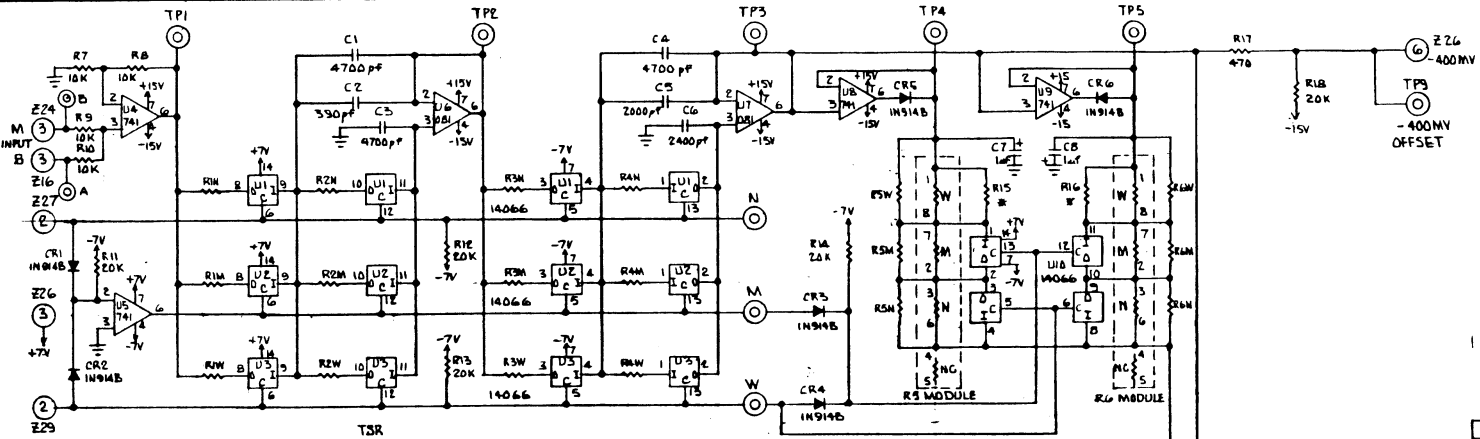


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

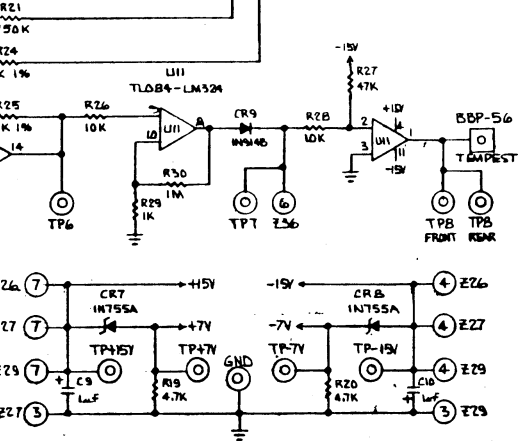
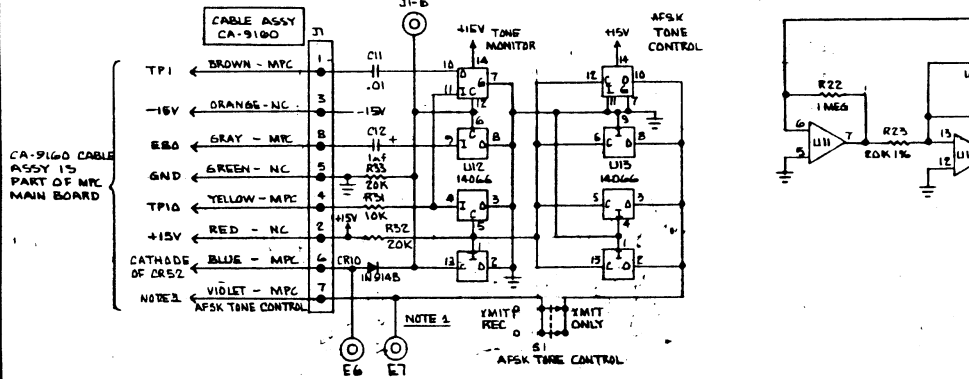


TSK MONITOR SECTION
 with U12 (14066) installed and J1 Cable Assembly connected to mainboard, the AFSK tone keyer output tones will be displayed in the SED Cross Display when terminal unit is in the Transmit (half-duplex) mode.

AFSK TONE CONTROL SECTION
 If U13 (14066) is also installed, the AFSK tone keyer output may be limited during receive and enabled when the terminal unit is in Transmit.

Reference Note 1:
 Connect J1-7 (violet):
 MPC-1000C and MPC-1000B: AFSK output connector.
 MPC-1000C/DK and MPC-1000B/DK: Junction of R148 and R149. R149 is not installed in C/DK and CR/DK units.
 MPC-1000T II: Rear-most R149 location. Front panel AFSK-OFF switch must be in UP (AFSK ON) position.

For AFSK Tone Control, both U12 and U13 must be installed. If only AFSK Tone Control is required, R6 and R7 may be hard-wired to the main board in lieu of using the 8 wire CA-9160 cable assembly.

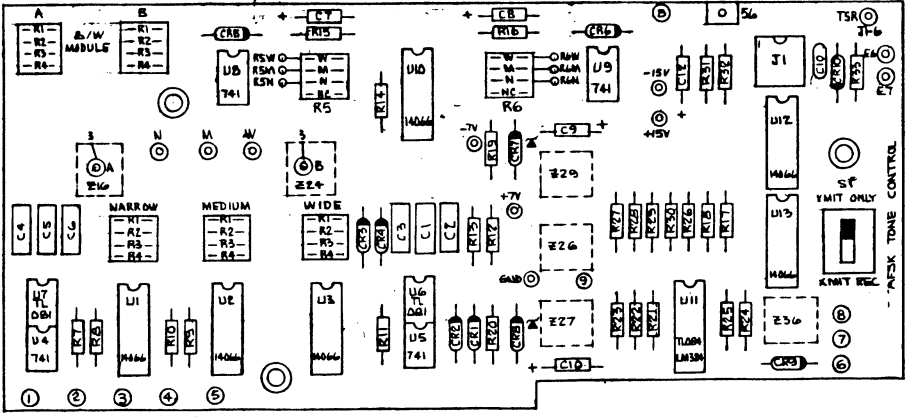


ITEM	QTY	PART NO.	REF. DESIGN.	DESCRIPTION	MFR.
37	1	Z56Z66LE	S1	SWITCH, 8-POS, 8-PDT, PC	GMETRCRAFT
38	5	CA-DBPF-II	RES. MODULES	HEADER, 8-PIN, BIFURCATED	CA
39	2	CA-14SLP-105D	U4, 5, 16, 7	IC SOCKET, 16 PIN, LOW PROF	CA
33	7	CA-14SLP-105D	U1, 2, 3, 10-13	IC SOCKET, 14 PIN, LOW PROF	CA
32	2	CA-BSLP-105D	U8, 9	IC SOCKET, 8 PIN, LOW	CA
31	6	CA-DBP-II	W, W, N, R5, R6, 7, 1	IC SOCKET, 8 PIN, HIGH	CA
30	6	CA-DBPF-II	R16, 24, 26, 27, 29, 36	HEADER, 8 PIN	CA
29	1	TL084	U11	IC, QUAD OP-AMP	T.I. NATIONAL
27	4	741	U4, 5, 6, 9	IC, MINI-DIP, OP-AMP	T.I.
26	2	TL081	U6, 7	IC, MINI-DIP, OP-AMP	T.I.
25	6	14066	U12, 13, 19, 12, 19	IC, QUAD BILATERAL SWITCH	MOTOROLA
24	1	METAL FILM	R23	RESISTOR, 20K, 1%	DALE
23	2	METAL FILM	R24, 25	10K, 1%	DALE
22	2	CARB FILM	R22, 30	1M, 1/4W, 5%	E-DMN
21	1	R21		750K	
20	1	R27		47K	
19	7			1/4W, 1/8, 32, 53	
18	7			R7-D, 28, 28, 31	
17	2			R19, 20	
16	1	R19		1K	
15	1	CARB FILM	R17	RESISTOR, 470, 1/4W, 5%	E-DMN
14					
13					
12	2	1N755A	CR7, 8	DIODE, ZENER, 7.5V	ITT
11	2	1N914B	CR1-6, 9-10	DIODE, SIGNAL SIL.	IRC
10					
9					
8	5	TANTALUM	C7-10, 12	CAPACITOR, 1uF, 35V	SPRAGUE
7	1	CER DISC	C11	.01uF	DLECTRON
6	1	DM15	C2	330 pF	C.D.
5	1	DM19	C3	2000 pF	C.D.
4	1	DM19	C6	2400 pF	C.D.
3	3	J650	C1, 3, 4	CAPACITOR, 4700 pF	FLESSY
2	1	CA9160		CABLE ASSY	C.A.
1	1	75350B		P.C. BOARD	DAVETRON

R5-85 MODULES			
W	H	BAUD	BAUD
1	1	150	100
1	1	150	110
1	1	150	120
1	1	150	130
1	1	150	140
1	1	150	150
1	1	150	160
1	1	150	170
1	1	150	180
1	1	150	190
1	1	150	200
1	1	150	210
1	1	150	220
1	1	150	230
1	1	150	240
1	1	150	250
1	1	150	260
1	1	150	270
1	1	150	280
1	1	150	290
1	1	150	300
1	1	150	310
1	1	150	320
1	1	150	330
1	1	150	340
1	1	150	350
1	1	150	360
1	1	150	370
1	1	150	380
1	1	150	390
1	1	150	400
1	1	150	410
1	1	150	420
1	1	150	430
1	1	150	440
1	1	150	450
1	1	150	460
1	1	150	470
1	1	150	480
1	1	150	490
1	1	150	500

LOW PASS FILTER	
BAUD RATE	INDUCTOR
150	1.00
110	1.50
100	2.00
90	2.50
80	3.00
70	3.50
60	4.00
50	4.50
40	5.00
30	5.50
20	6.00
15	6.50
10	7.00
5	7.50

MARK II



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APPROVALS: DATE: 1/20/75
 SCALE: 2:1
 SHEET 1 OF 1

REVISIONS: 1
 DATE: 1/20/75
 APPROVED: [Signature]

PROJECT: BDP-100
 TITLE: BINARY BIT PROCESSOR