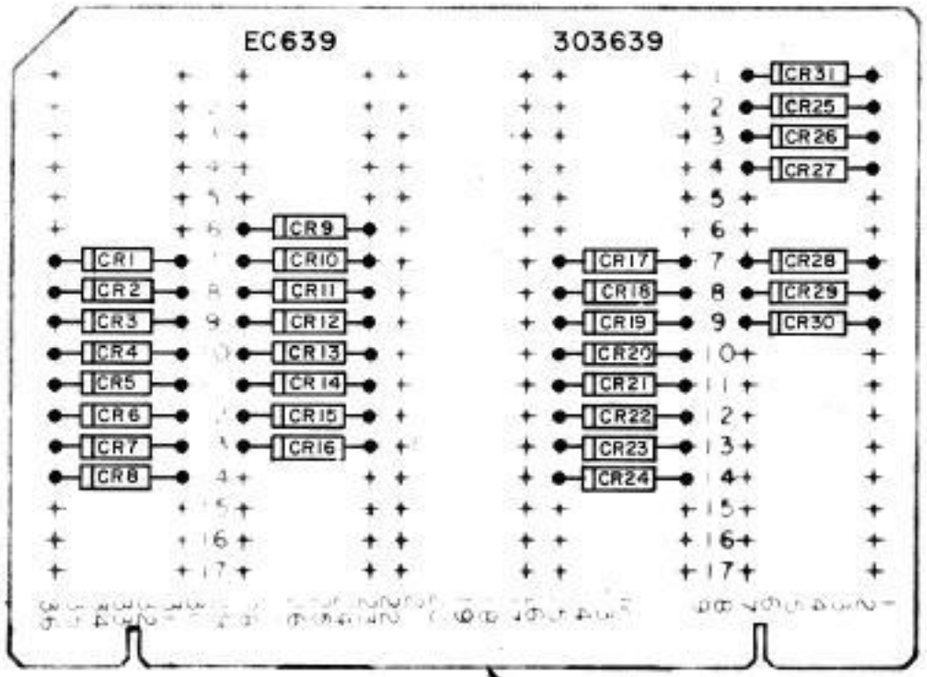
EC639

303639

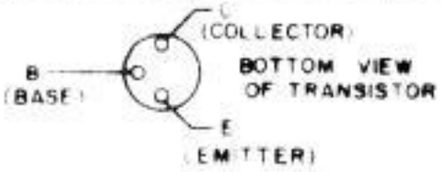
8 INPUT AND GATE (3)
4 INPUT AND GATE (1)
3 INPUT AND GATE (1)

CIRCUIT BOARD EC639



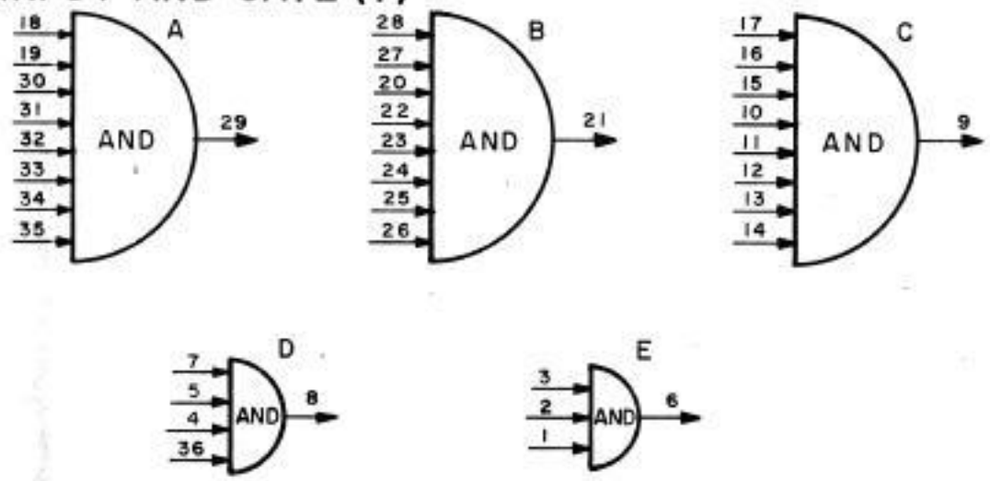
NOTE:

REFER TO 5050 WD FOR BASIC MARKING INFORMATION



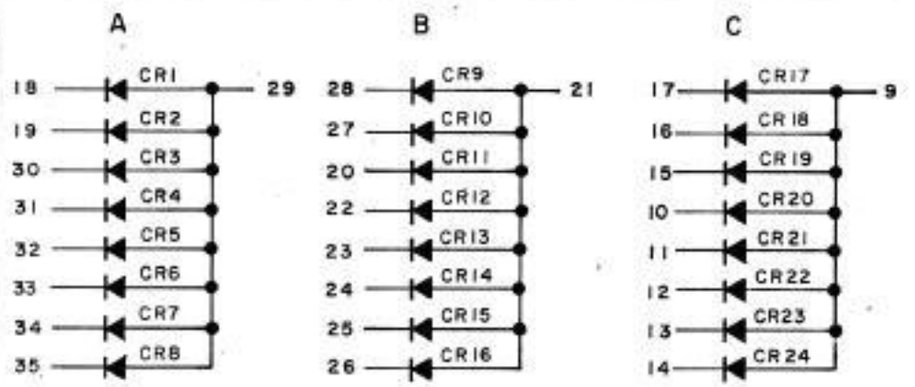
FIVE "AND" GATES ARE PROVIDED. THREE HAVE 8 INPUTS, ONE HAS 4 INPUTS, AND ONE HAS THREE INPUTS. ALL GATES FUNCTION IDENTICALLY. A -6 VOLT LEVEL ON ANY INPUT RESULTS IN A -6 VOLT OUTPUT FROM THE CORRESPONDING GATE. WHEN ALL INPUTS TO A PARTICULAR GATE ARE AT 0 VOLTS. THE CORRESPONDING OUTPUT IS 0 VOLTS. AN EXTERNAL BIAS RESISTOR TO +6 VOLTS IS REQUIRED. WHEN USED AS A "FAN-OUT" GATE, THE OUTPUT OF THE "AND" GATE BECOMES THE INPUT OF THE "FAN-OUT" GATE AND THE INPUT OF THE "AND" GATE BECOMES THE OUTPUT OF THE "FAN-OUT" GATE. WHEN INPUT OF THE "FAN-OUT" GATE IS 0 VOLTS ALL OUTPUTS ARE 0 VOLTS. WHEN THE INPUT IS -6 VOLTS ALL OUTPUTS ARE ISOLATED FROM EACH OTHER.

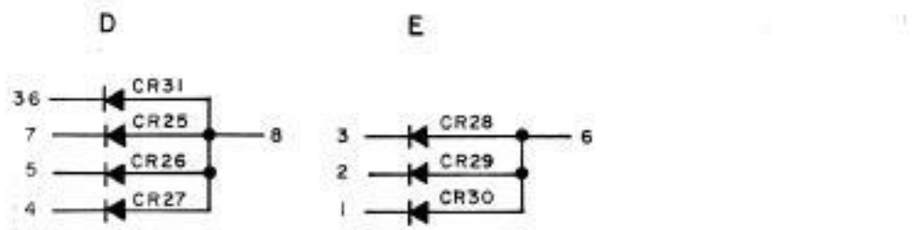
SYMBOL



NOTE: WHEN USED AS "FAN-OUT" GATE "AND" IS REPLACED BY "F/O" IN SYMBOL.

WHEN TWO OR MORE GATES ARE TIED TOGETHER ONE SYMBOL CAN BE USED SHOWING PROPER TERMINAL DESIGNATIONS.





ALL DIODES ARE D2

REVISIONS

ISSUE	DATE	AUTH NO

REF DESIGN	TELETYPE PART NO	TOTAL QTY	NAME AND DESCRIPTION	LOCATING FUNCTION	REF DESIGN	TELETYPE PART NO	TOTAL QTY	NAME AND DESCRIPTION	LOCATING FUNCTION
CR1-CR31	177108	31	DIODE D2	GATE					
EC	302702	1	CIRCUIT CARD, ETCHED						

APPROVALS	
E AND R	F OF M
F NUMBER	
PROJ NO 303639	
DATE 12-16-63	
PD FILE NO G-A.353.219A	
DRAWN R.W.V.	CHKD
ENGR J.M.G.	APPD

TELETYPE CORPORATION

R.W. Voight